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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/759,639	01/16/2004	Ayedin Nikazm	16356.834 (DC-05396)	1548
27683 HAYNES AND	7590 05/23/200 D BOONE, LLP	EXAMINER		
901 Main Street		ELAMIN, ABDELMONIEM I		
Suite 3100 Dallas, TX 7520	02		ART UNIT	PAPER NUMBER
			2116	
			MAIL DATE	DELIVERY MODE
			05/23/2008	PAPER

## Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)
	10/759,639	NIKAZM ET AL.
Office Action Summary	Examiner	Art Unit
	Abdelmoniem Elamin	2116
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with the c	correspondence address
A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING DESTRICTION - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATION  .136(a). In no event, however, may a reply be tired will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDONE	N. nely filed the mailing date of this communication. ED (35 U.S.C. § 133).
Status		
1) ☐ Responsive to communication(s) filed on 29 A  2a) ☐ This action is <b>FINAL</b> . 2b) ☐ This  3) ☐ Since this application is in condition for allowated closed in accordance with the practice under	is action is non-final. ance except for formal matters, pro	
Disposition of Claims		
4)	awn from consideration.	
Application Papers		
9) The specification is objected to by the Examin 10) The drawing(s) filed on is/are: a) accomposed and applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Examination.	cepted or b) objected to by the drawing(s) be held in abeyance. Section is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority document application from the International Bureat* See the attached detailed Office action for a list	nts have been received. nts have been received in Applicat ority documents have been receive au (PCT Rule 17.2(a)).	ion No ed in this National Stage
Attachment(s)  1) Notice of References Cited (PTO-892)  2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) Information Disclosure Statement(s) (PTO/SB/08)  Paper No(s)/Mail Date	4)  Interview Summary Paper No(s)/Mail D 5)  Notice of Informal F 6)  Other:	ate

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## **DETAILED ACTION**

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on Apr. 29, 2008 has been entered.

## Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1, 3-4, 6-11, 13, 15-16, 18-24 are rejected under 35 U.S.C. 102(b) as being anticipated by Massie et al, US. Pat. No. 6,144,114 (*cited in PTO 892 mailed on 10/5/2006*).
- 4. Claims 1, 13 and 24, Massie teaches an information handling system (IHS) [computer server arrangement, col. 1, lines 10-11], comprising:
  - a system board including a processor [inherent];
  - a first battery for supplying power to the system board [power supply A];
  - a second battery for supplying power to the system board [power supply B]; and
- a switching circuit coupled to the first battery the second battery and the system board  $[GT_A]$  and  $GT_B$ , for repeatedly switching between the first battery and the second battery for

supplying power to the system board, the switching circuit receiving only one input from the first battery [ $GT_A$  receives only one input from Power supply A] and only one input from the second battery [ $GT_B$  receives only one input from Power supply B], each battery supplying a peak amount of current for periods of time during which the switching circuit has connected one of the batteries for supplying current while, simultaneously, the other of the batteries supplies no current whereby, in the aggregate, the batteries maintain a continuous supply of peak current to

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a first diode coupled in series with the first battery [Di of Fig. 7, which is part of  $GT_A$  of Fig. 1], the switching circuit, and the system board, wherein the first diode is located between the switching circuit and the system board, and wherein the first diode prevents reverse flow current from the second battery to the first battery while the second battery is supplying power to the system board [col. 9, lines 9-11];

the system [see Figs. 1 and 2A and related disclosure];

a second diode [Di of Fig. 7, which is part of  $GT_B$  of Fig. 1] coupled in series with the second battery, the switching circuit, and the system board, wherein the second diode is located between the switching circuit and the system board, and wherein the second diode prevents reverse flow current from the first battery to the second battery while the first battery is supplying power to the system board;

wherein at no time during operation are both the first and second batteries connected for supplying current [see timing diagram of Fig. 2A];

wherein the switching circuit connects the first battery to supply power to the system board during first periods of time [e.g.,  $t_0$  of Fig. 2A] alternating with second periods of time

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during which the switching circuit connects the second battery to supply power to the system board [e.g.,  $t_1$  of Fig. 2A]; and

wherein the first time periods are equal in duration to the second time periods [equal duration of 0.01 msec., see col. 4, lines].

- 5. Claims 3, 15, Massie teaches the peak power that can be drawn from the first battery during the first time periods is greater than the power that the first battery is capable supplying under a continuous load [because using the battery to power the load all the time wears it out].
- 6. Claims 4, 16, Massie teaches the peak power that can be drawn from the second battery during the second time periods is greater than the power that the second battery is capable of supplying under a continuous load [because using the battery to power the load all the time wears it out].
- 7. Claims 6-7, 18-19, Massie teaches the first time periods are greater/shorter in duration than the second time periods [col. 4, lines 60+, see also the timing diagrams of Figs. 5-6].
- 8. Claims 8, 20, Massie teaches the switching circuit includes a field effect transistor (FET) switch [col. 3, line 29].
- 9. Claims 9, 21, Massie teaches the switch operates in response to a switching signal generator [see, for example, gating signal GS-A signal of Fig. 1].
- 10. Claims 10, 22, Massie teaches the switching signal generator exhibits a variable switching frequency [see switching circuit  $GT_A$  and  $GT_B$  of Fig. 1 and related disclosure].
- 11. Claims 11, 23, Massie teaches a capacitor coupled to the switching circuit, wherein the capacitor is for stabilizing the voltage supplied to the system board [see, for example, Capacitor  $C_A$  of Fig. 1].

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Claim Rejections - 35 USC § 103

12. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all

obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of

this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter

as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which

said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

13. Claim 12 is rejected under 35 U.S.C. 103(a) as being unpatentable over Massie et al, US.

Pat. No. 6,144,114 (cited in PTO 892 mailed on 10/5/2006).

14. Claim 12, Massie fails to teach the IHS is a portable HIS.

However, Examiner asserts that portable HISs are well known in the art. These types of

limitations are considered field of use, and are not patentably distinct.

Response to Arguments

Applicant's arguments filed on Mar. 27, 2008 have been fully considered but they are not

persuasive for the following reasons:

14. In the remarks, Applicant argues as follows:

"the controller of Massie receives two signals the power input at D and the enable signal at the

controller C, from each of the power supplies ..." [see pages 9-10, REMARKS].

15. Examiner responses as follows:

The switching circuit of Massie  $[GT_A \text{ and } GT_B]$  receives only one input from the first

power supply and only one input from the second power supply as discussed in paragraph 3

above.

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Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Abdelmoniem Elamin whose telephone number is 571-2727-

3674. The examiner can normally be reached on MON - THUR 10:00 AM - 6::00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Rehana Prrveen can be reached on 571-272-3676. The fax phone number for the

organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would

like assistance from a USPTO Customer Service Representative or access to the automated

information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Abdelmoniem Elamin/ Primary Examiner, Art Unit 2116

January 29, 2008